IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

| In re U.S. Patent No. US 6,700,794 B2 VINSON ET AL. |) |
|---|---|
| Issued: MARCH 2, 2004 |) |
| For: DECOUPLING CAPACITOR CLOSELY COUPLED WITH INTEGRATED CIRCUIT |) |

REQUEST FOR CERTIFICATE OF CORRECTION OF OFFICE MISTAKE UNDER RULE 1.322(a)

Mail Stop Office of Publications Certificate of Corrections Branch Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Certificate

APR 0 6 2004

of Correction

Sir:

In accordance with the provisions of Rule 1.322(a) of the Rules of Practice, the Patent Office is respectfully requested to correct matters in the above-identified patent in accordance with the Certificate of Correction Form PTO-1050 attached hereto.

The Patent Office is requested to issue a Certificate of Correction on the above-referenced Patent Document, and to place such a Certificate of Correction in the file, so that such will appear on any copies of the patent which are ordered in the future.

The corrections being made are of consequence to provide the proper recitation in the claims. Moreover, since these errors are believed to be errors of the Patent

In re Patent of VINSON ET AL.

Patent No. US 6,700,794 B2

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Office, such changes should be made at no expense to the Patentee. No new matter is added by these corrections.

Respectfully submitted,

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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: COMMISSIONER FOR PATENTS, P.O. BOX 1450, ALEXANDRIA, VA 22313-1450 on this day of April, 2004.

Justin Dow

UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO.

: US 6,700,794 B2

DATED

March 2, 2004

INVENTOR(S)

Robert S. Vinson; Joseph B. Brief; Donald J. Beck,

and Gregory M. Jandzio

It is certified that error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below

Column 6, Line 63

Delete: "a decoupling"

Tı

Insert: -- at least one decoupling --

Column 6, Line 63

Delete: "on each"

Insert: -- on the --

Column 7, Lines 4-

Delete: "to a die pad and from a die pad to a substrate

bonding pad"

5

Insert: -- to the die pads and from the die pads to the

substrate bonding pads --

Column 7, Line 19

Delete: "a wire bond"
Insert: -- the wire bond --

Column 8, Line 10

Delete: "to a logic pin and from a logic pin"

Insert: -- to the logic pin and from the logic pin --

Column 8, Line 11

Delete: "a substrate"

Insert: -- the substrate --

MAILING ADDRESS OF SENDER:

PATENT NO. <u>US 6,700,794 B2</u>

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